

ABSTRACT OF THE DISCLOSURE

A liquid crystal display includes a bottom insulating substrate. Gate lines and common electrode lines are formed on the substrate parallel to each other, and storage capacitor electrodes are branched from the common electrode lines. A gate insulating layer is formed on the gate lines, the common electrode lines, and the storage capacitor electrodes, data lines are formed on the gate insulating layer, and a protective layer is formed on the data lines. Pixel electrodes are formed on the protective layer with first opening patterns. A top insulating substrate faces the bottom substrate with a black matrix and a common electrode, and the common electrode is provided with second opening patterns. Each pixel electrode completely covers the storage capacitor electrodes at the regions where the second opening pattern is overlapped with the sides of the pixel electrode.